

REMARKS

Claims 1-31 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to under 37 CFR 1.83(a).

The Examiner requires that the means for generating a different voltage must be shown in the drawings. In this connection, the Examiner's attention is directed to element 110 in Figure 1.

It is respectfully submitted that the drawings are in full compliance with 37 CFR 1.83.

Claims 1-3, 5, and 16-18 were rejected under 35 U.S.C. § 103 as being unpatentable over Bazarjani in view of Hester; and Claims 7, 8, 22, and 23 were rejected under 35 U.S.C. § 102(b) as being anticipated by Hester.

These rejections are respectfully traversed.

It is respectfully submitted that Bazarjani does not disclose or suggest the presently claimed invention including the step of converting the difference voltage into a P-bit digital code wherein P is less than N in independent Claim 1, the SAR logic then generating a first P-bit digital code wherein P is less than N in independent Claim 7, the means for converting the difference voltage into a P-bit digital code wherein P is less than N in independent Claim 16, albeit defined as the SAP by sending the intermediate P-bit value in each P iteration wherein P is less than N in independent Claim 22.

Bazarjani does not disclose this aspect.

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Hester does not disclose or suggest the presently claimed invention including the step of converting the difference voltage in to P-bit digital code wherein P is less than N in the various forms in independent Claims 1, 7, 16, and 22.

Hester does not relate to this aspect.

Applicants appreciate the indication that if Claims 6, 8-16, 19-21, and 24-31 were rewritten in independent form including the limitations of the base claim and any intervening claims, these claims will be allowable.

By the instant amendment, Claim 6 has been amended to include the limitations of only Claim 1.

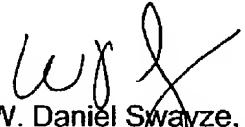
Claim 19 has been amended to include the limitations of only Claim 16 and Claim 24 has been amended only to include the limitations of Claim 22.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



W. Daniel Swayze, Jr.
Attorney for Applicant
Reg. No. 34,478

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5633

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